

WEST

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[Main Menu](#) [Search Form](#) [Posting Counts](#) [Show S Numbers](#) [Edit S Numbers](#) [Preferences](#) [Cases](#)

Search Results -

Term	Documents
MEMORY.DWPI,TDBD,EPAB,JPAB,USPT.	1118184
MEMORIES.DWPI,TDBD,EPAB,JPAB,USPT.	127906
MEMORYS.DWPI,TDBD,EPAB,JPAB,USPT.	150
CONTROLLER.DWPI,TDBD,EPAB,JPAB,USPT.	839686
CONTROLLERS.DWPI,TDBD,EPAB,JPAB,USPT.	79604
PREFETCH.DWPI,TDBD,EPAB,JPAB,USPT.	4271
PREFETCHES.DWPI,TDBD,EPAB,JPAB,USPT.	1008
PREFETCHING.DWPI,TDBD,EPAB,JPAB,USPT.	2561
PREFETCHINGS.DWPI,TDBD,EPAB,JPAB,USPT.	4
((MEMORY ADJ CONTROLLER) ADJ4 (PREFETCHING OR PREFETCH)).USPT,JPAB,EPAB,DWPI,TDBD.	34
((MEMORY CONTROLLER) ADJ4 (PREFETCH OR PREFETCHING)).USPT,JPAB,EPAB,DWPI,TDBD.	34

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DATE: Friday, February 22, 2002 [Printable Copy](#) [Create Case](#)

Set Name Query
side by sideHit Count Set Name
result set

DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

L4 (memory controller) adj4 (prefetch or prefetching)

34 L4

L3 L2

362 L3

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

L2 (memory controller or controller) same buffer same (prefetch or prefetching)

370 L2

L1 5623628.pn.

3 L1

END OF SEARCH HISTORY

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Term	Documents
(20 AND 6).USPT,JPAB,EPAB,DWPI,TDBD.	93
(L20 AND L6).USPT,JPAB,EPAB,DWPI,TDBD.	93

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Database:**Search:**

L21

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Set Name Query
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DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L21</u>	l20 and l6	93	<u>L21</u>
<u>L20</u>	harvard architecture	475	<u>L20</u>
<u>L19</u>	L18 and l17	3	<u>L19</u>
<u>L18</u>	l8 and l7	68	<u>L18</u>
<u>L17</u>	l16 and l15	7408	<u>L17</u>
<u>L16</u>	second adj4 memory adj4 (instruction or code or data)	11772	<u>L16</u>
<u>L15</u>	first adj4 memory adj4 (instruction or code or data)	14636	<u>L15</u>
<u>L14</u>	first adj4 memory adj4 (instruction or code)	2342	<u>L14</u>
<u>L13</u>	seperate same memory same (instruction or code) same (data or operand)	5	<u>L13</u>
<u>L12</u>	seperate adj4 memory adj4 (instruction or code) adj4 (data or operand)	0	<u>L12</u>
<u>L11</u>	L10	52	<u>L11</u>
<u>L10</u>	l5 and l6	52	<u>L10</u>
<u>L9</u>	l8 and l7 and l5	0	<u>L9</u>
<u>L8</u>	data adj2 prefetch adj2 buffer	132	<u>L8</u>
<u>L7</u>	(instruction) adj2 prefetch adj2 buffer	263	<u>L7</u>
<u>L6</u>	(prefetch or prefetching)	5402	<u>L6</u>
<u>L5</u>	Harvard Architecture and l4	196	<u>L5</u>
<u>L4</u>	L3	7886	<u>L4</u>

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L3</u>	l1 and l2	8136	<u>L3</u>
<u>L2</u>	memory adj2 (data or operand)	104294	<u>L2</u>
<u>L1</u>	memory adj2 (instruction or code)	21407	<u>L1</u>

END OF SEARCH HISTORY